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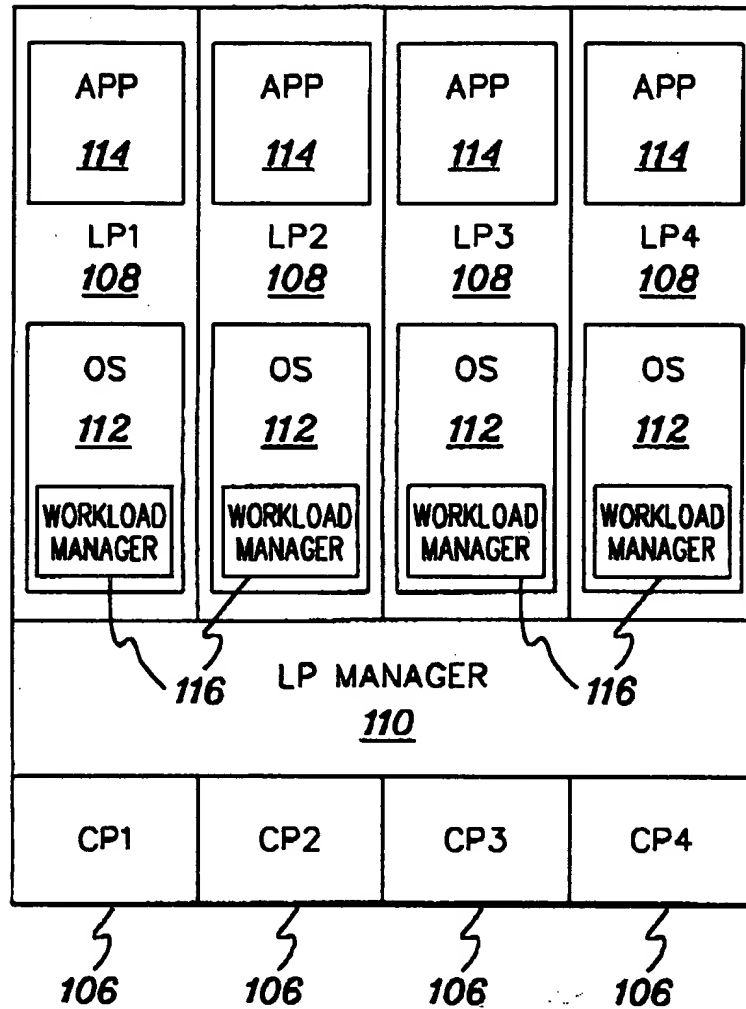


fig. 1A

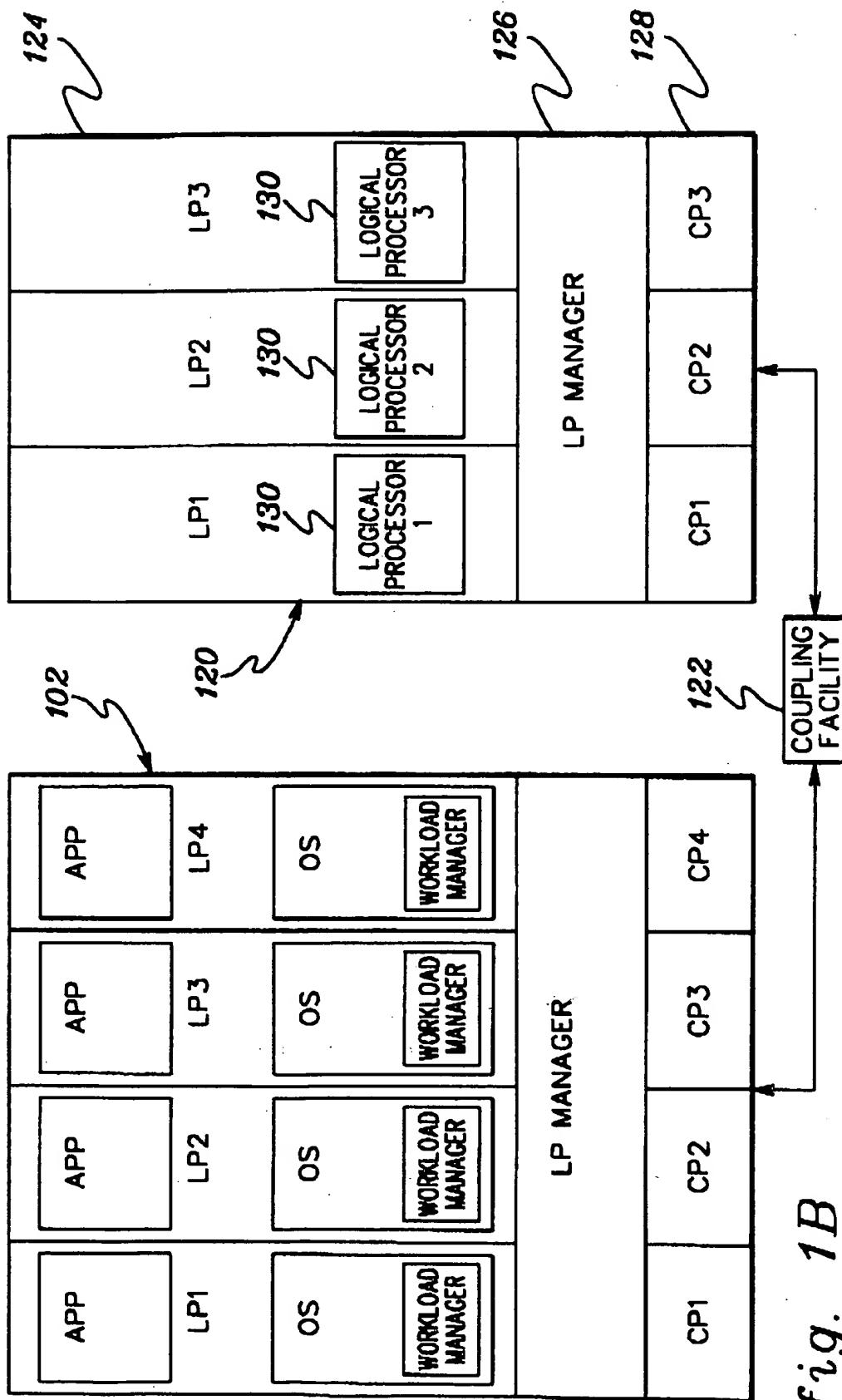


fig. 1B

669260-02430460

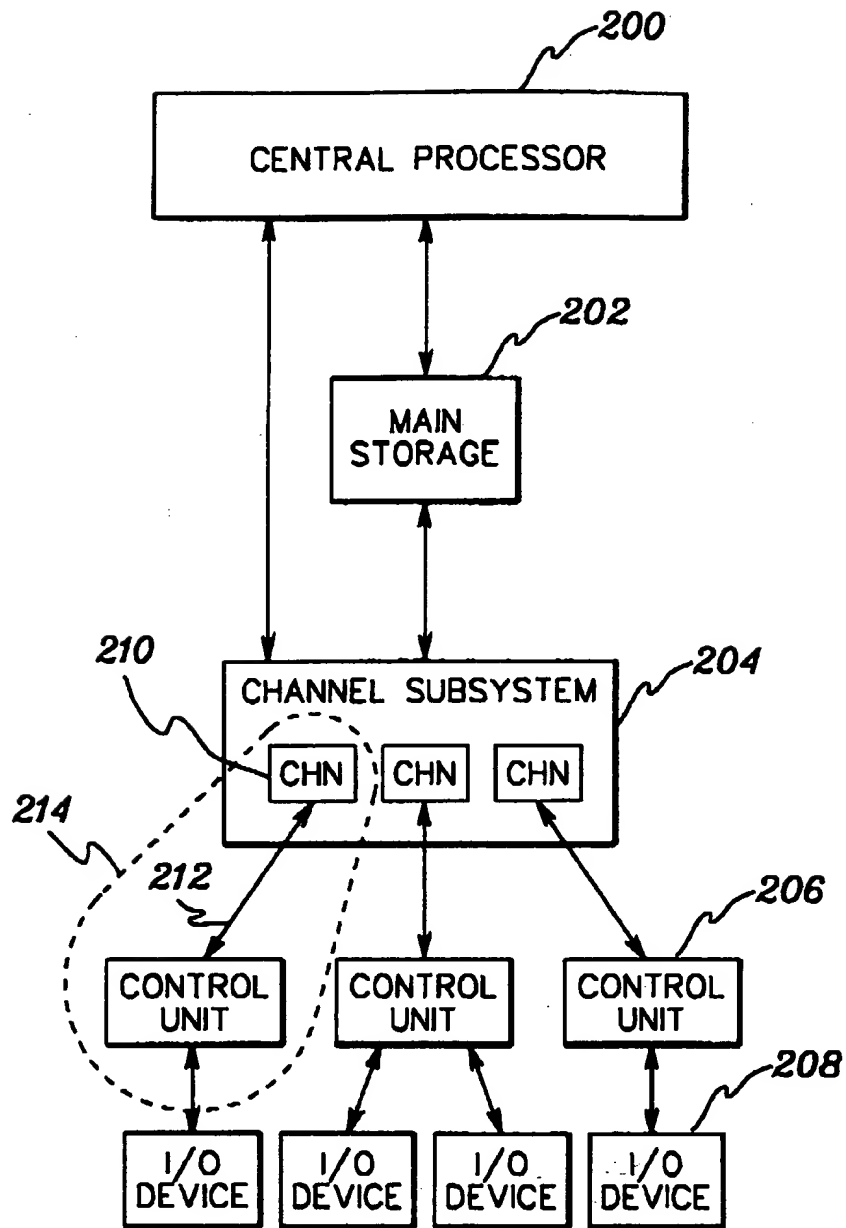


fig. 2

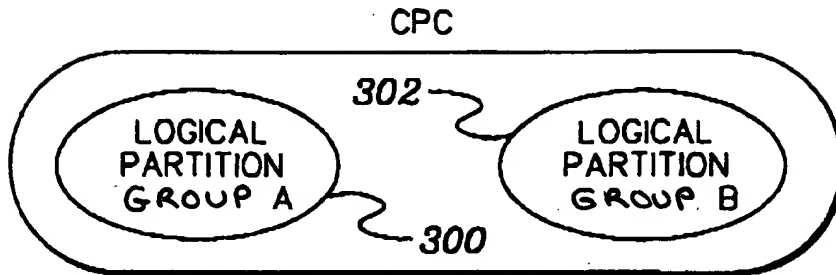


fig. 3

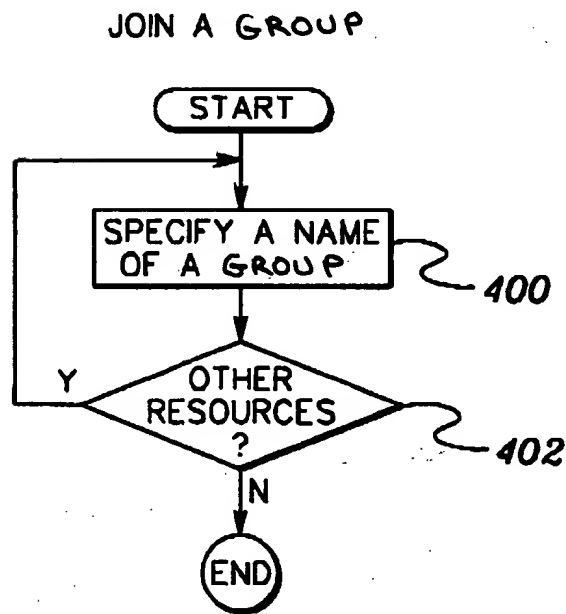
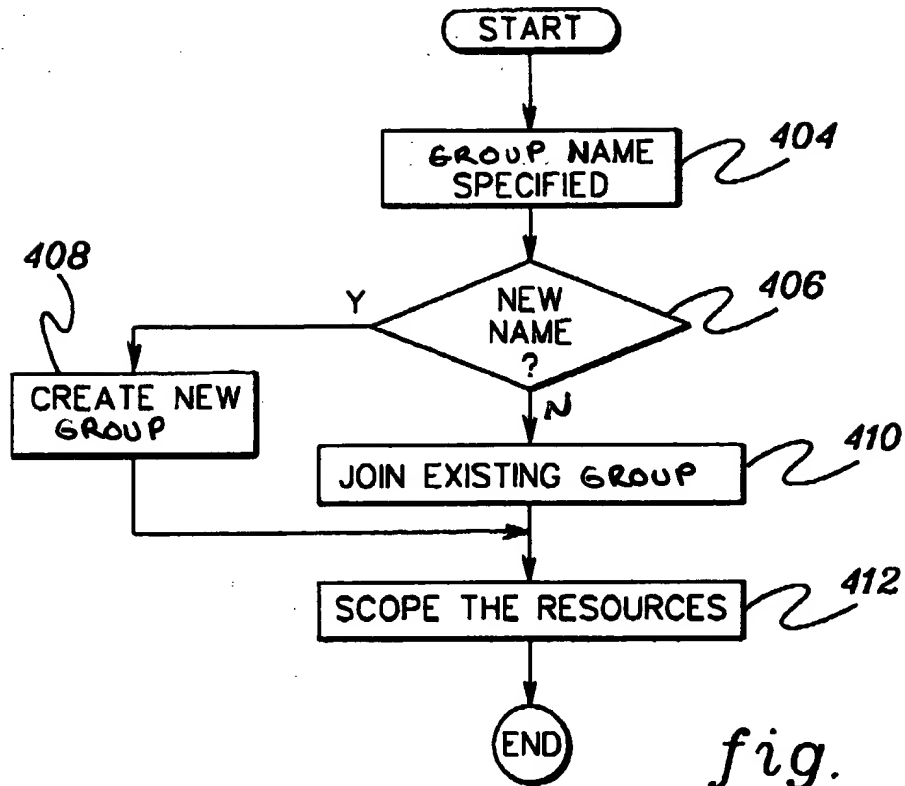
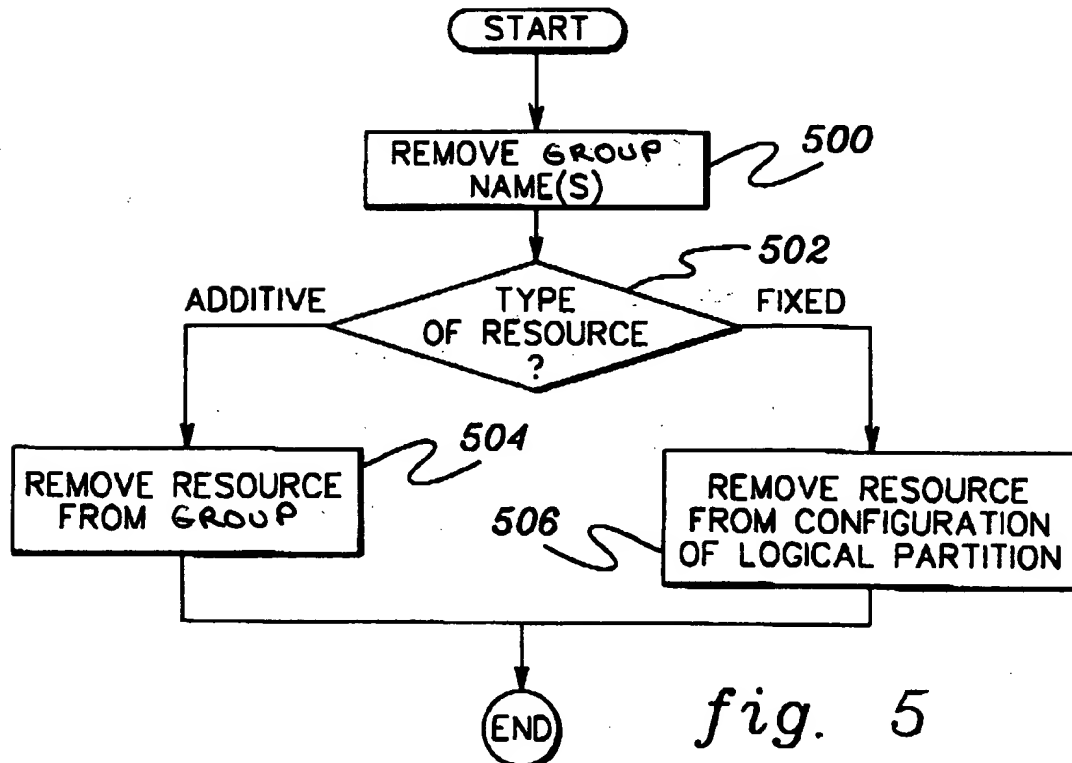


fig. 4A

663260-02480460



REMOVE FROM A GROUP



668260-0480460

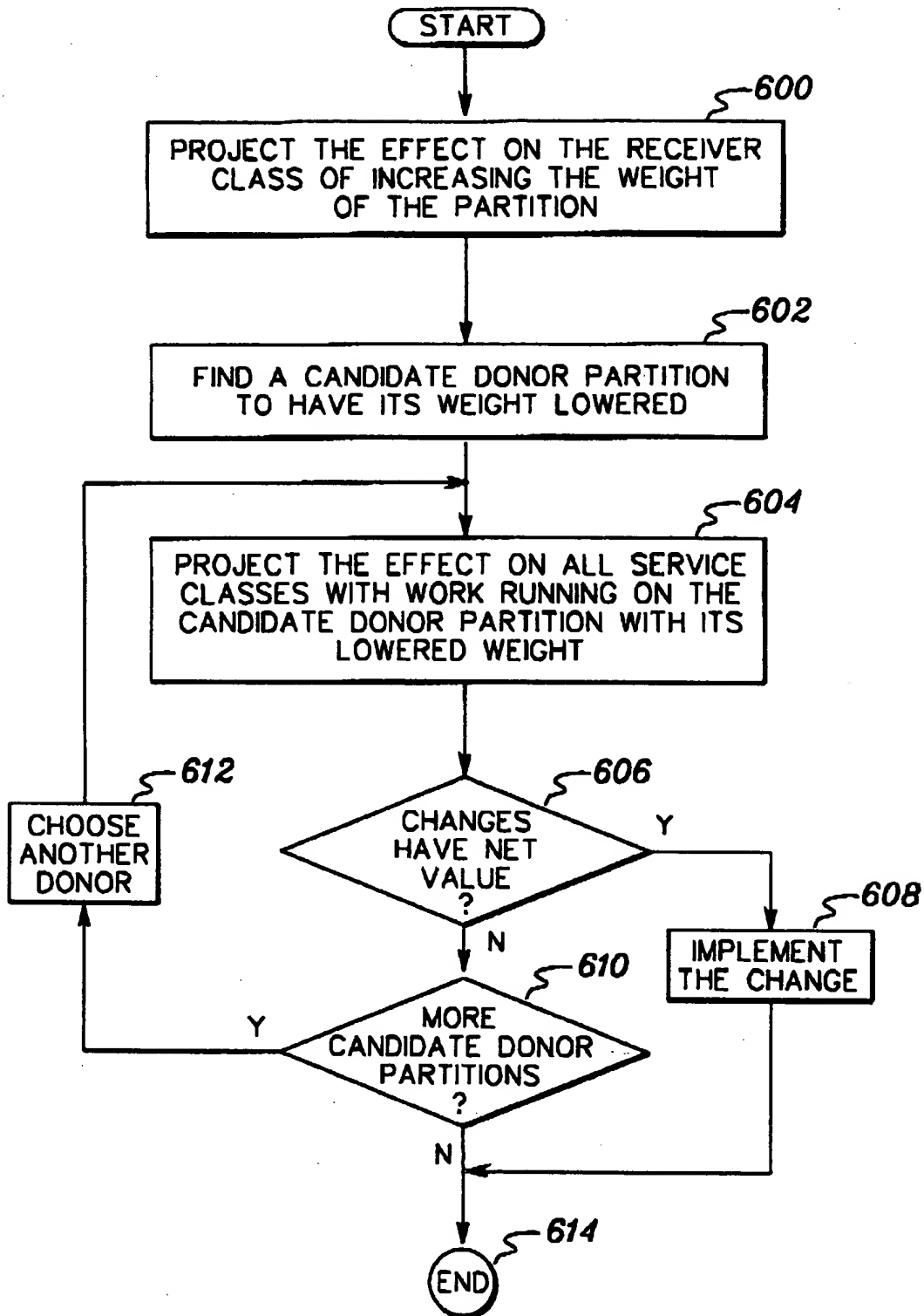


fig. 6

DYNAMIC ADJUSTMENT OF CONFIGURATION
OF LOGICAL PROCESSORS

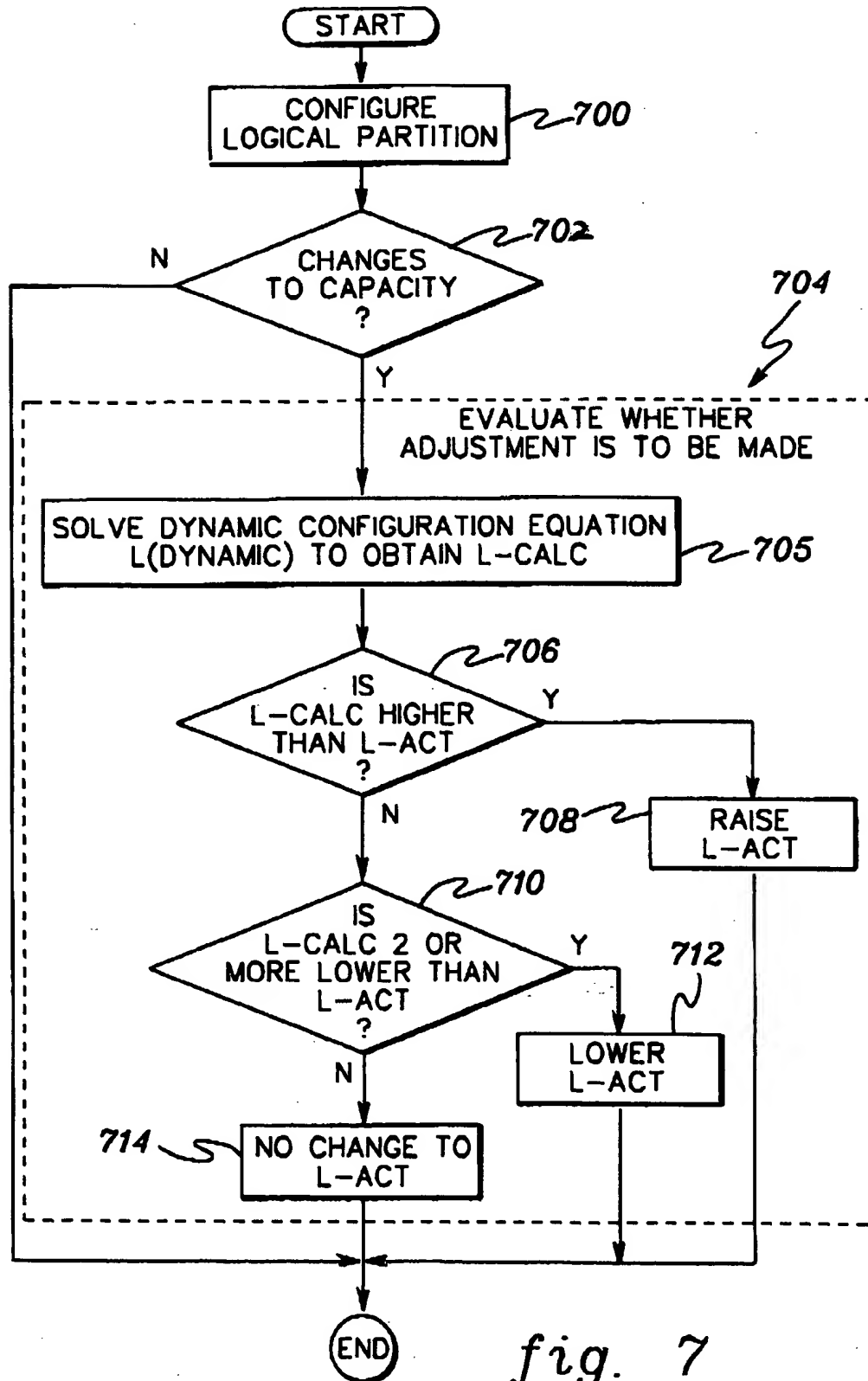


fig. 7

663260-02430450

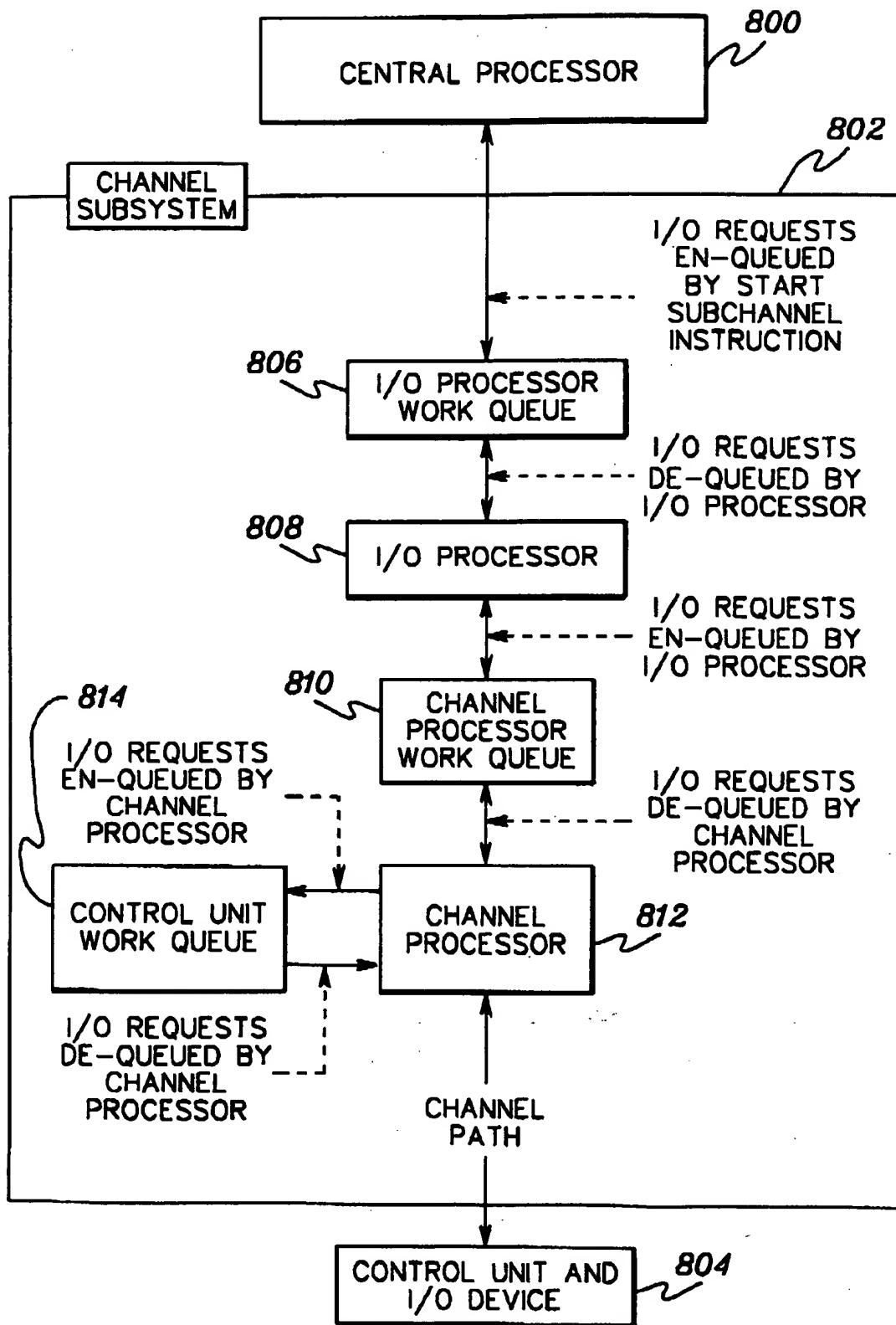


fig. 8

000000-02480460

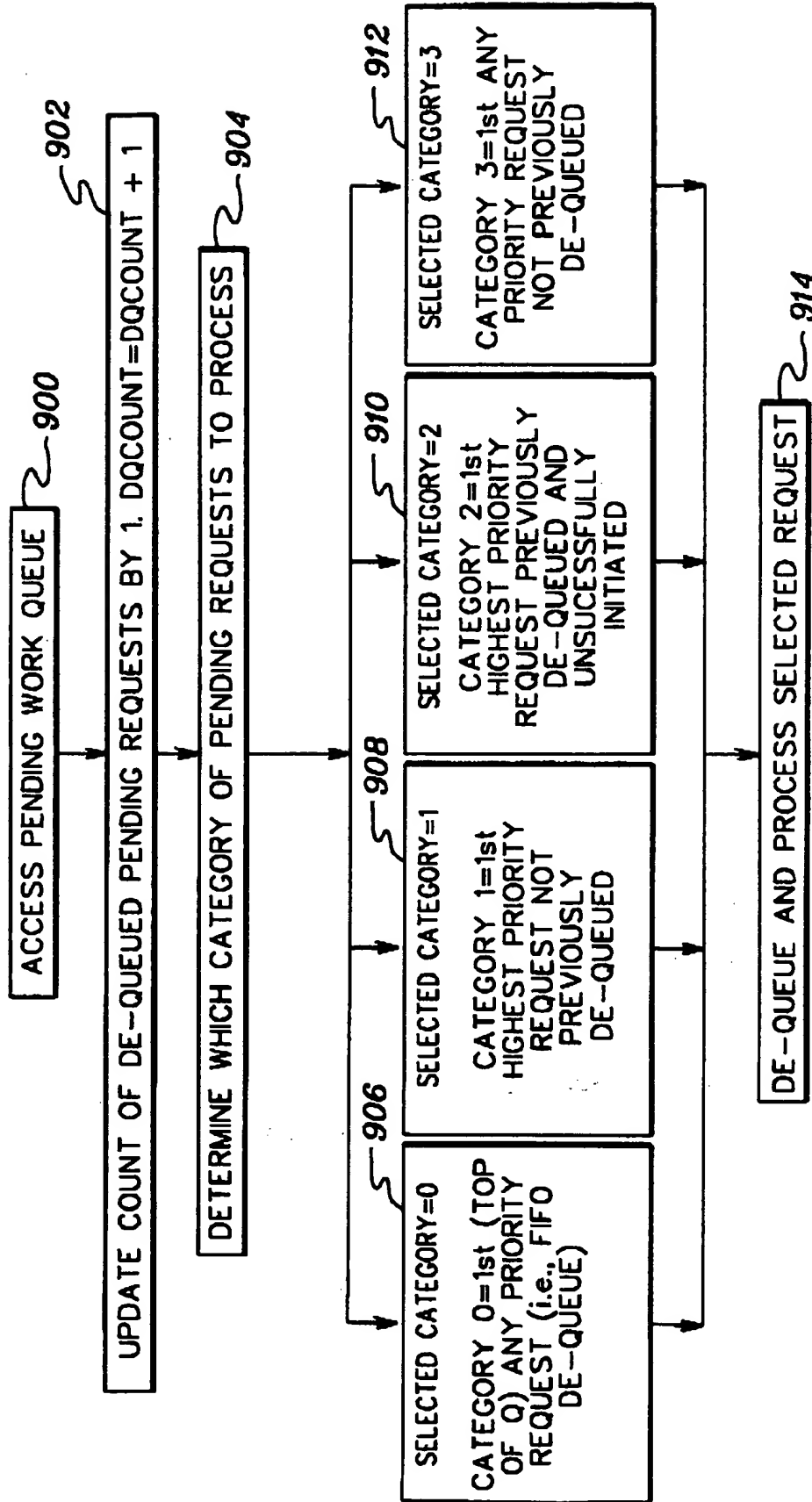


fig. 9

668250-02480450

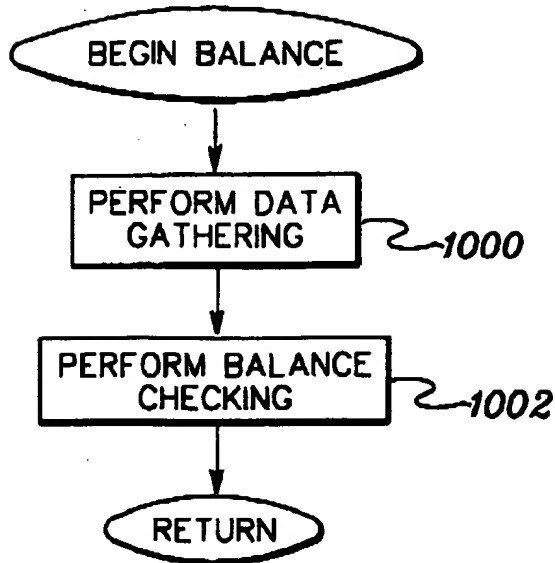


fig. 10

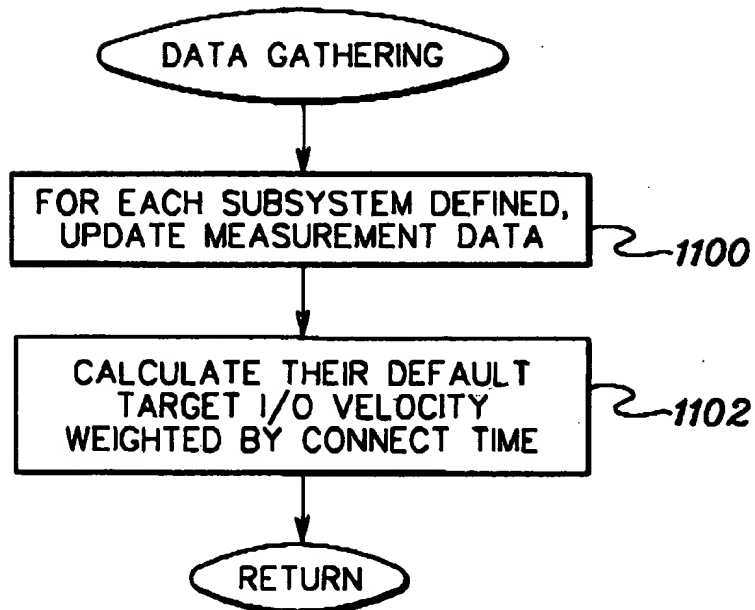


fig. 11

668260-02430450

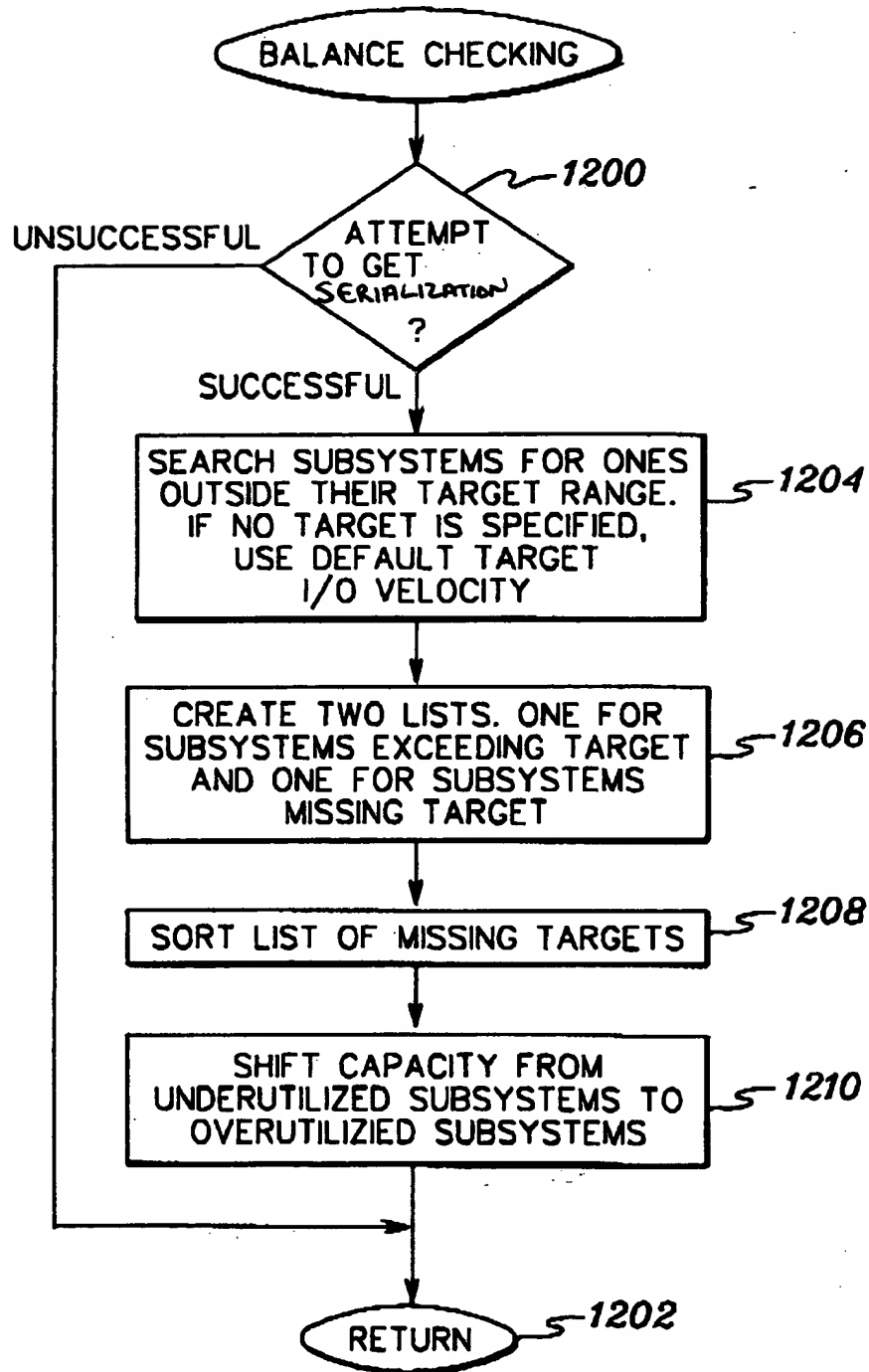
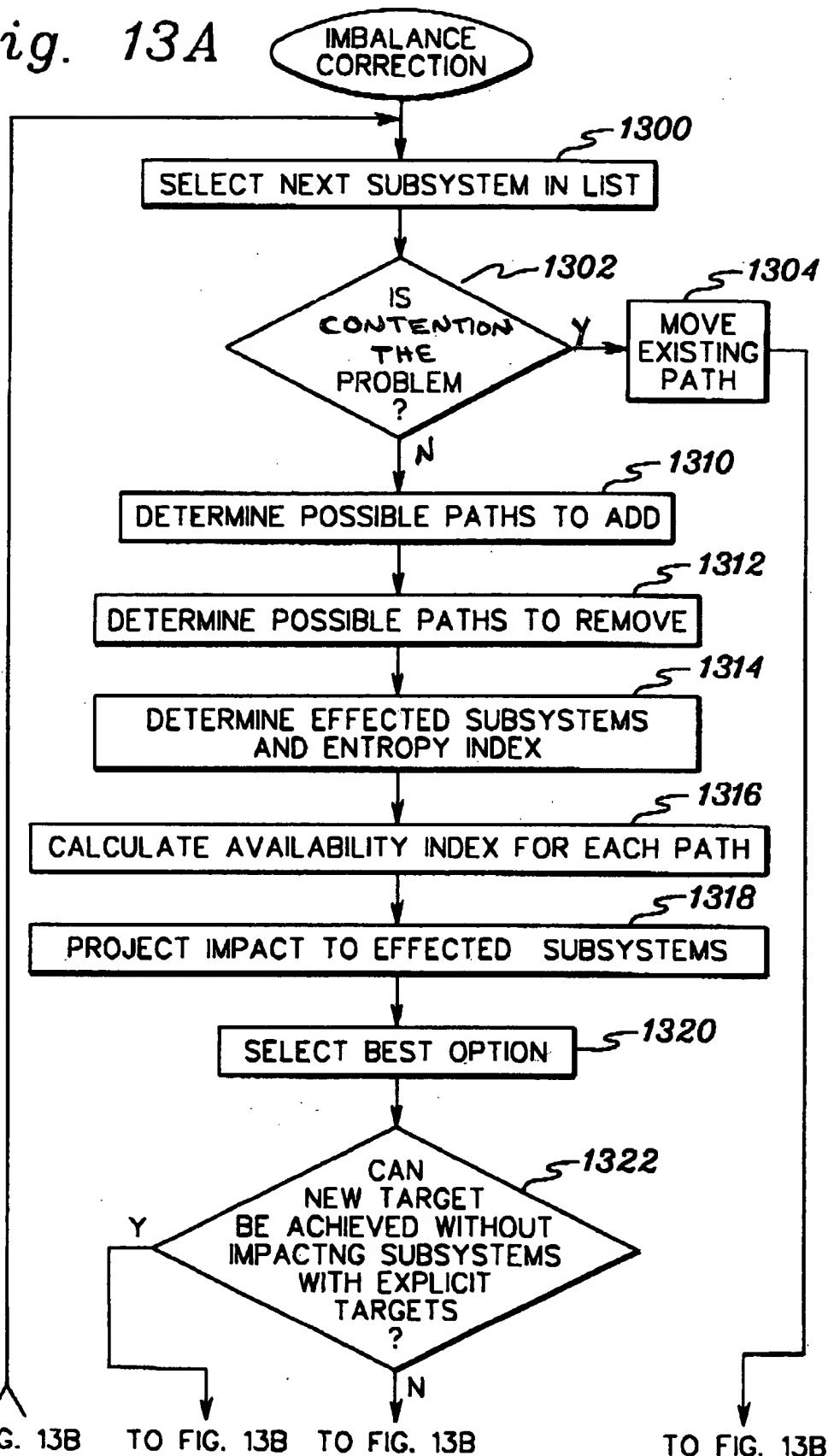


fig. 12

fig. 13A



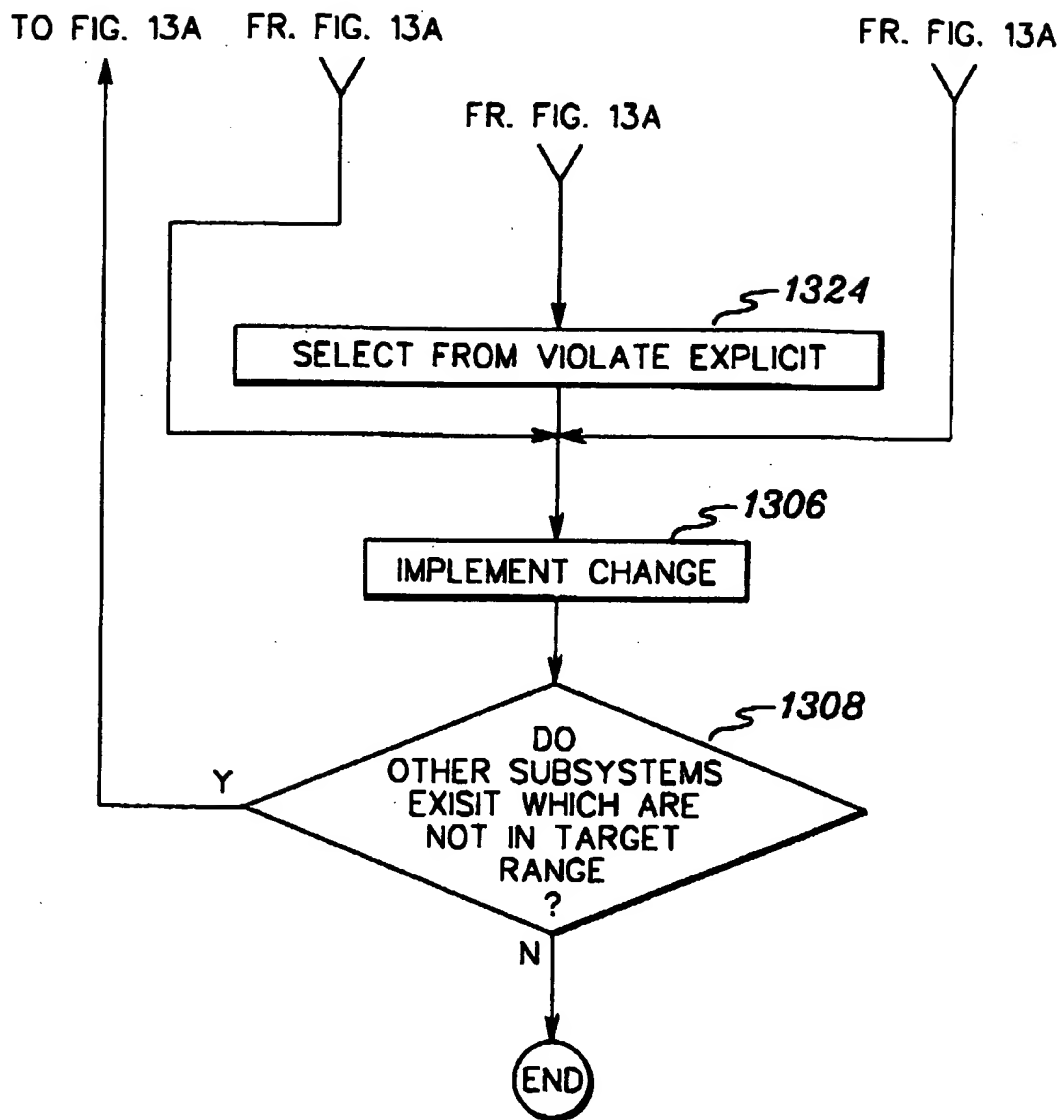


fig. 13B

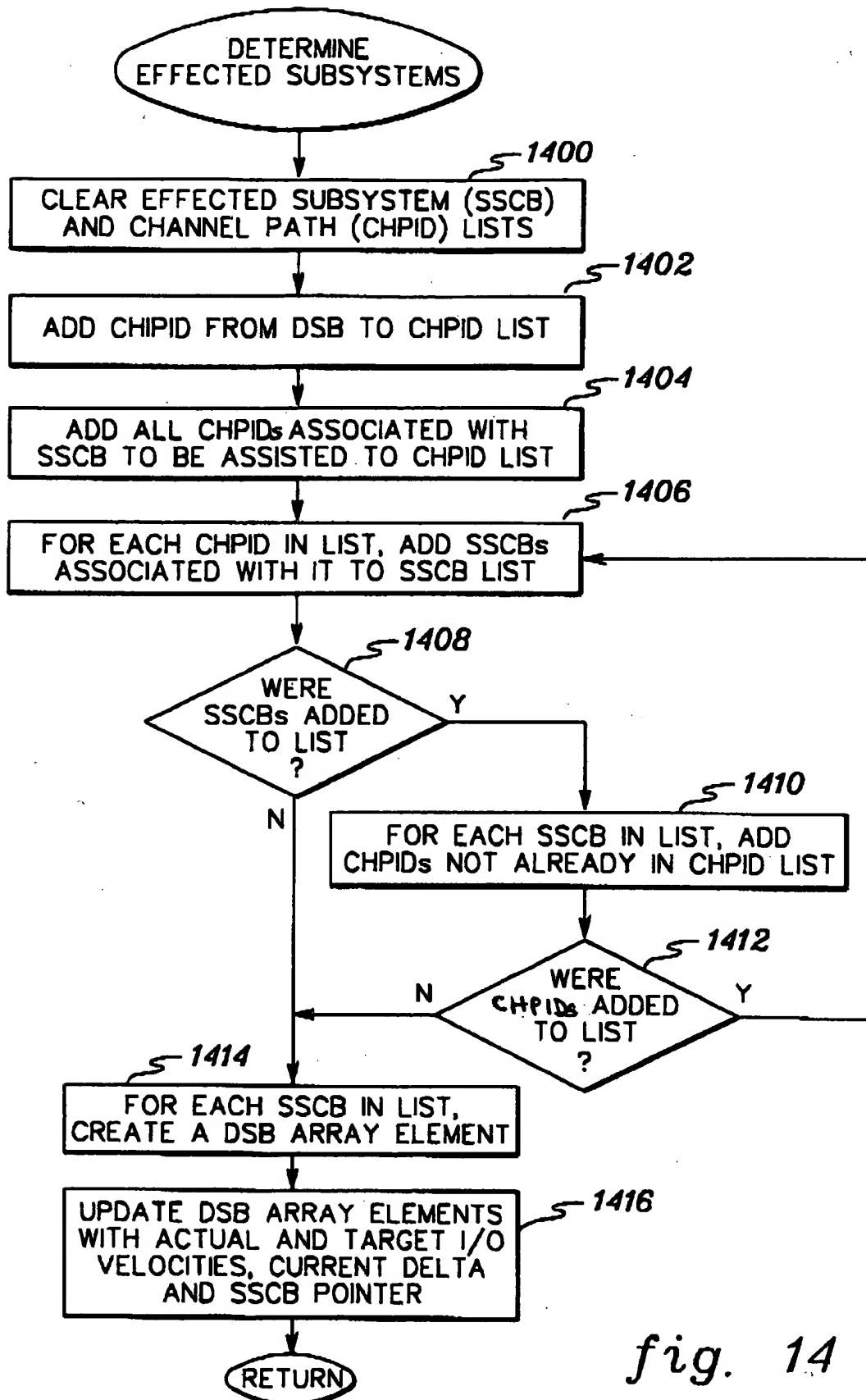


fig. 14

00000-02480460



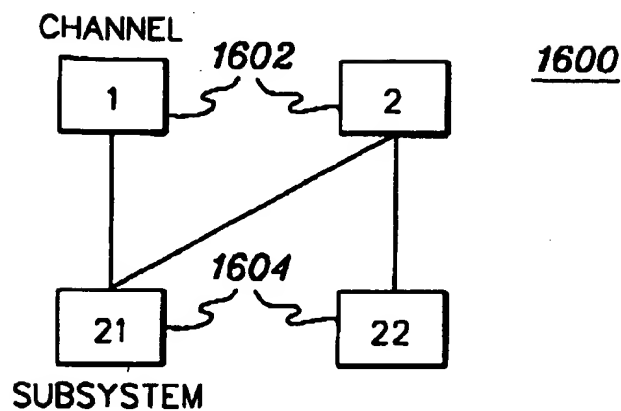


fig. 16A

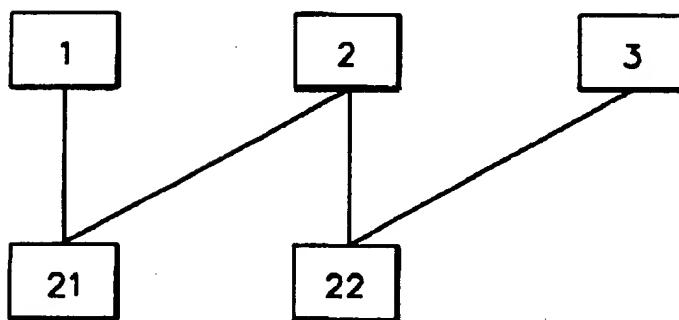


fig. 16B

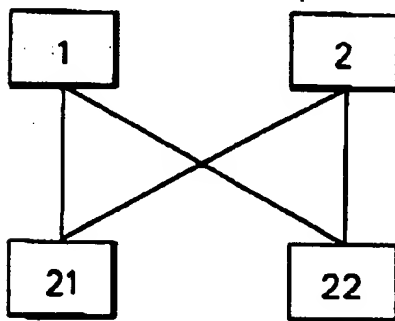


fig. 16C

668250-02480450

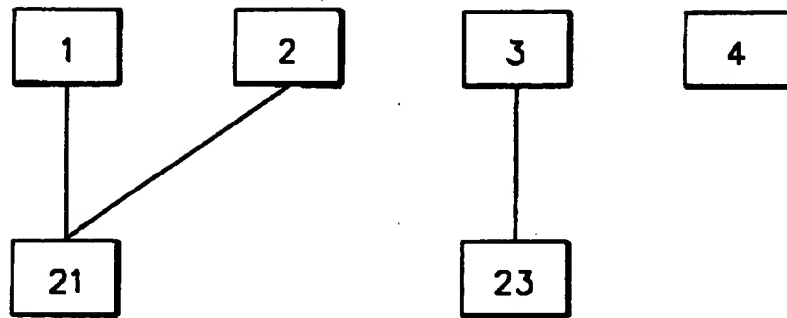


fig. 17A

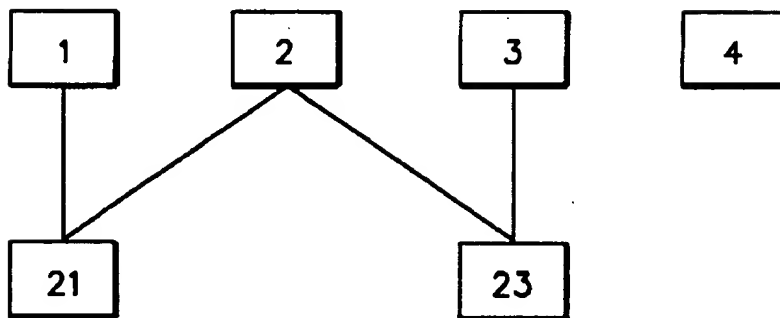


fig. 17B

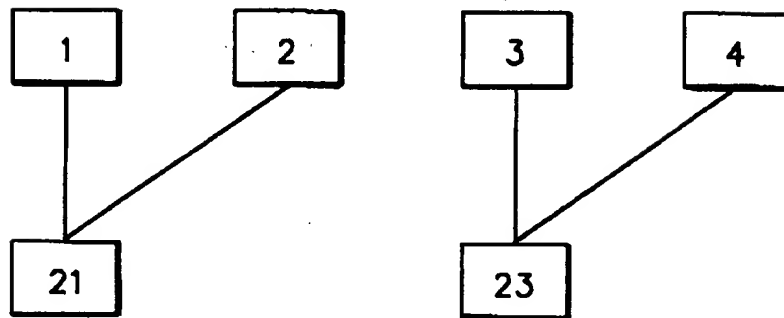


fig. 17C

668250-02480450

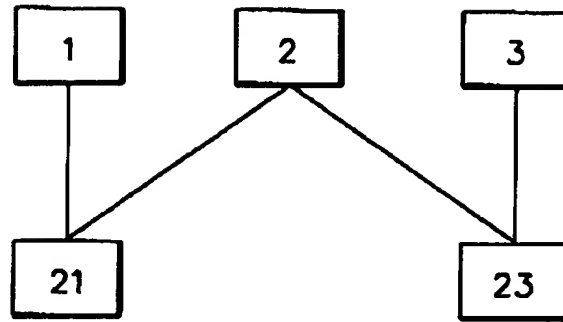


fig. 18A

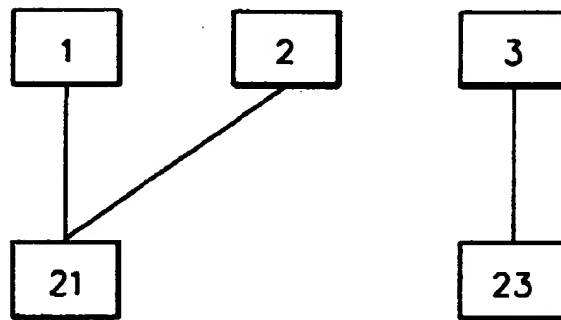


fig. 18B

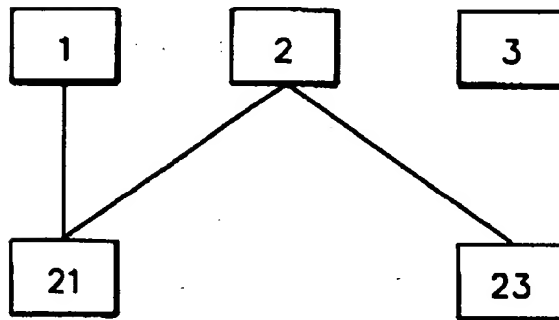


fig. 18C

669260-02480450

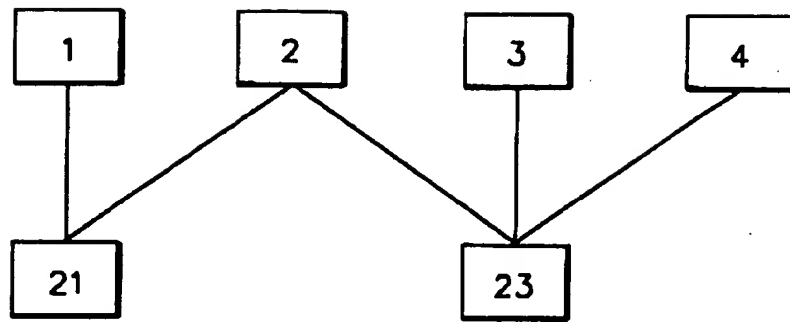


fig. 19A

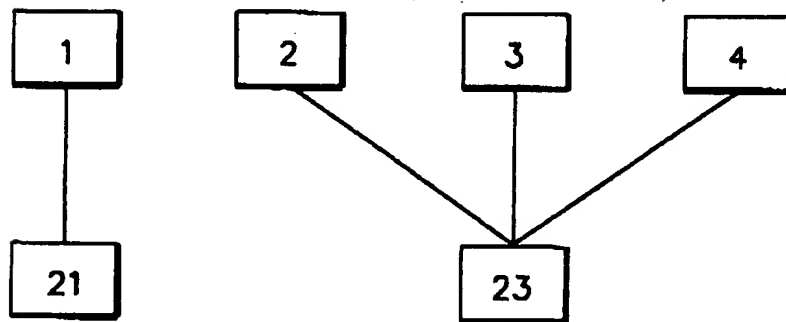


fig. 19B

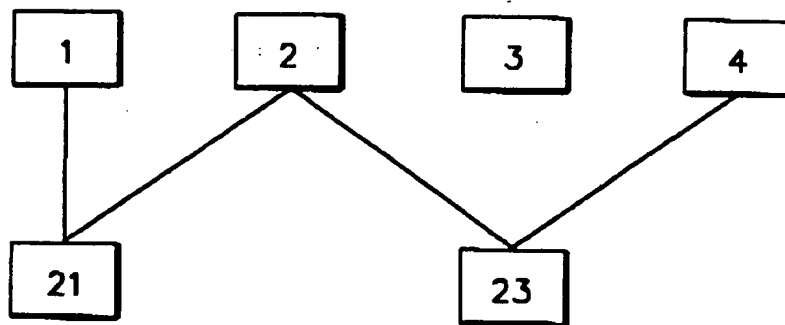
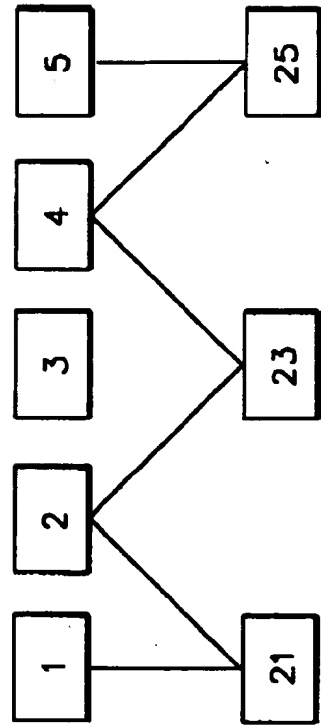
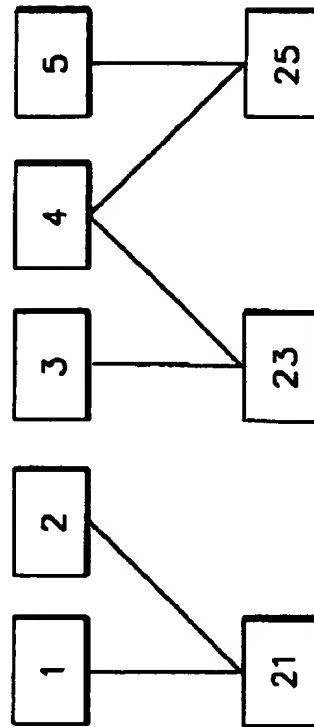
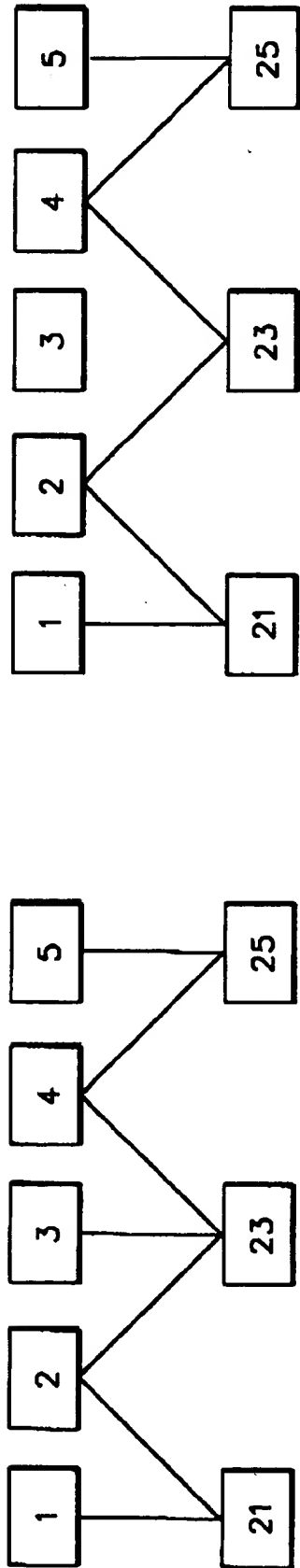


fig. 19C

668260-02480460



669260-0430460

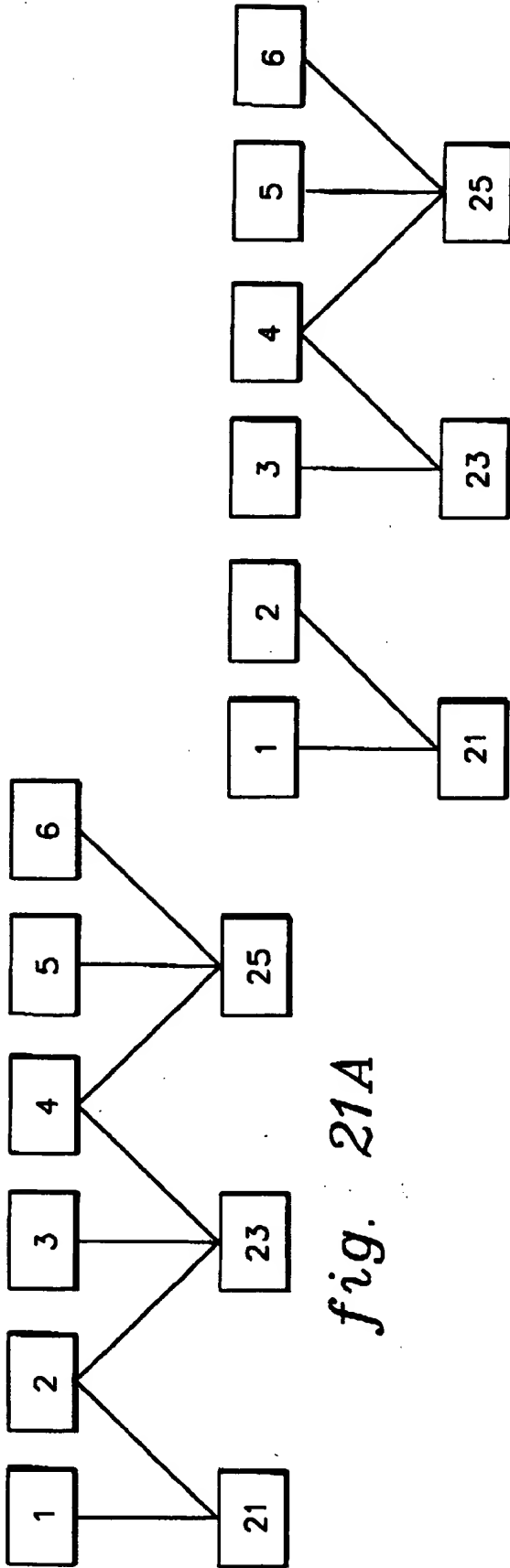


fig. 21A

fig. 21B

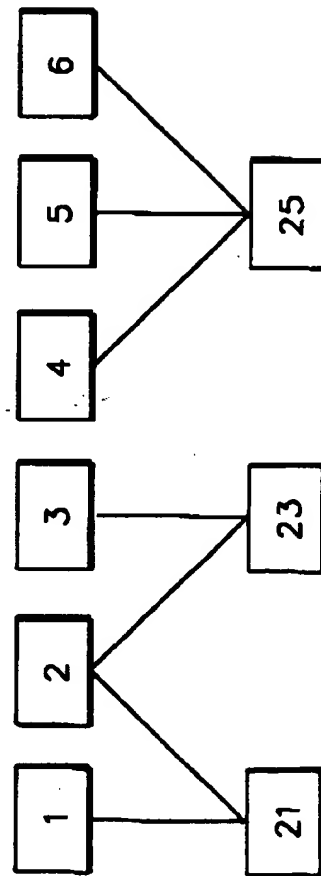


fig. 21C

668260-02430450

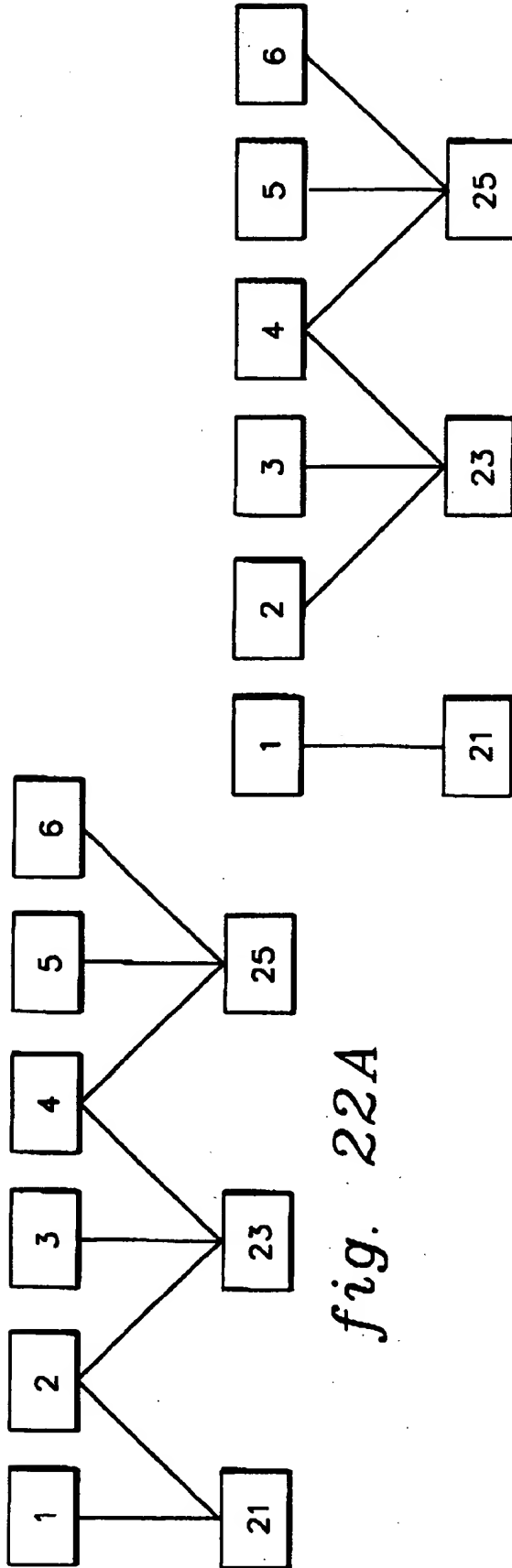


fig. 22A

fig. 22B

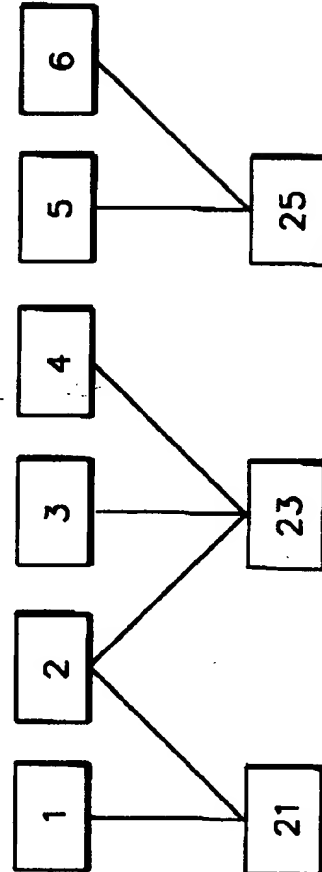


fig. 22C